



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. Lakshmi Viswanathan, et al. Art Unit: 2816
Serial No.: 10/749,275 Examiner: Jeffery S. Zweizig
Filed: 12/29/03 Docket: TI-36954
For: Load Sensing Voltage Regulator for PLL/DLL Architectures

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz
Karen Vertz

4-20-05
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are **THREE (3)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(972) 917-5466
Fax: (972) 917-4418

Respectfully submitted,

Alan K. Stewart
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Attorney for Applicants